



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Eiichi TAKAHASHI et al.

Group Art Unit: 2816

Application No.: 10/520,429

Docket No.: 122358

Filed: July 13, 2005

For: DIGITAL CIRCUIT HAVING DELAY CIRCUIT FOR ADJUSTMENT OF CLOCK
SIGNAL TIMING

REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the corrected filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,

James A. Oliff
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JAO:MAC/jeh

Date: October 19, 2006

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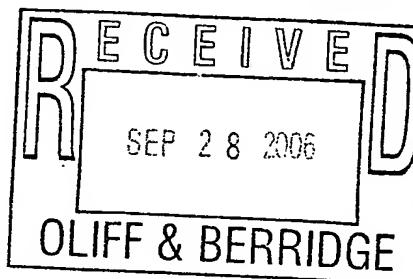
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KAM

APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/520,429	07/13/2005	2816	515	122358	19	9	1

25944
 OLIFF & BERRIDGE, PLC
 P.O. BOX 19928
 ALEXANDRIA, VA 22320



CONFIRMATION NO. 5260
 CORRECTED FILING RECEIPT

 TOC000000020595179*

Date Mailed: 09/27/2006

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Assignment For Published Patent Application

National Institute of Advanced Industrial Science and Technology, Tokyo, JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 25944.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP03/08648 07/08/2003

Foreign Applications

JAPAN 2002-200467 07/09/2002

If Required, Foreign Filing License Granted: 07/24/2006

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US10/520,429**

Projected Publication Date: Not Applicable

Non-Publication Request: No

Early Publication Request: No

** please see second page*

** SMALL ENTITY **



Title

Digital circuit having a delay circuit for adjustment of clock signal timing

Preliminary Class **Digital Circuit having delay circuit for adjustment of
327 Clock Signal timing**

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